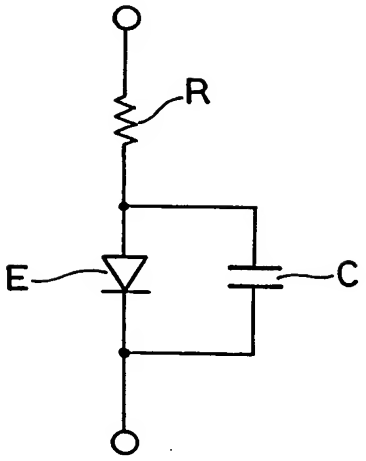
FIG.1 PRIOR ART



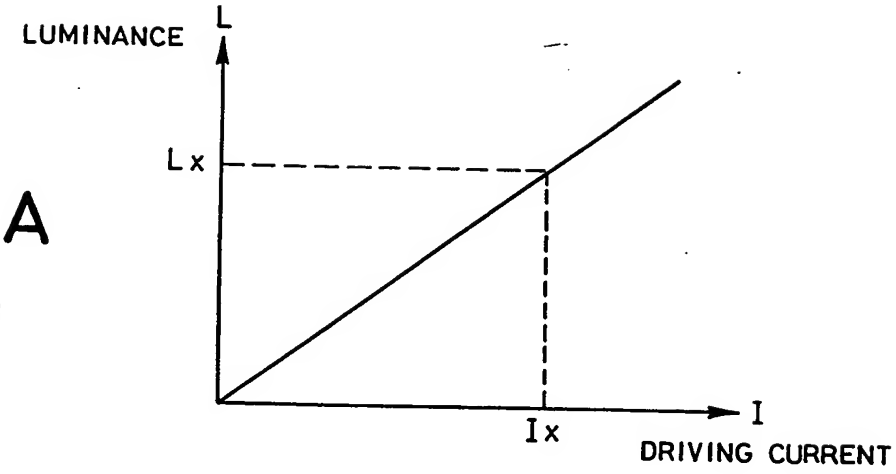


FIG. 2A

PRIOR ART

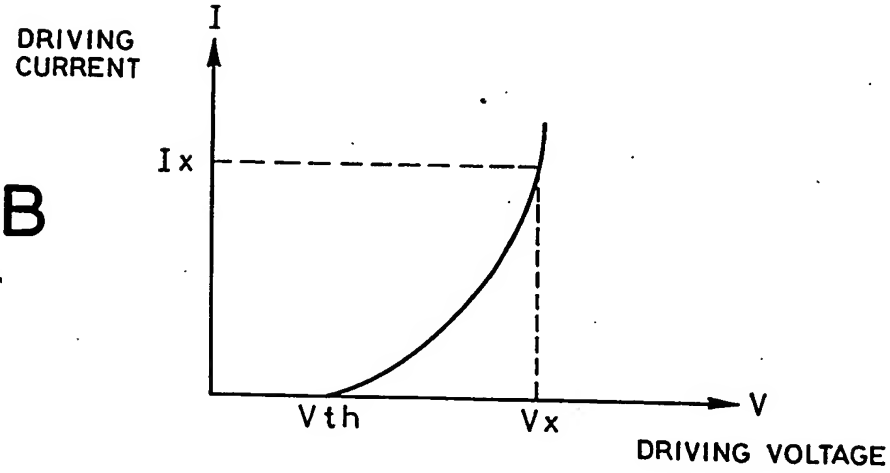


FIG. 2B

PRIOR ART

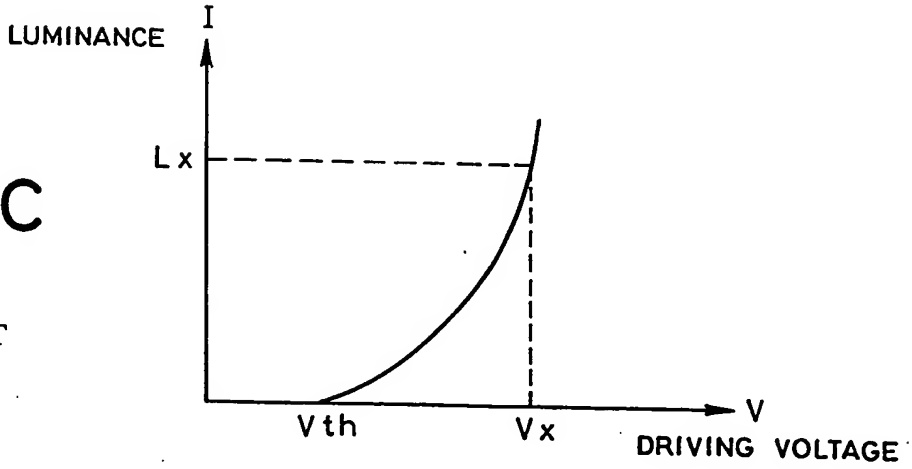


FIG. 2C

PRIOR ART

FIG. 3 PRIOR ART

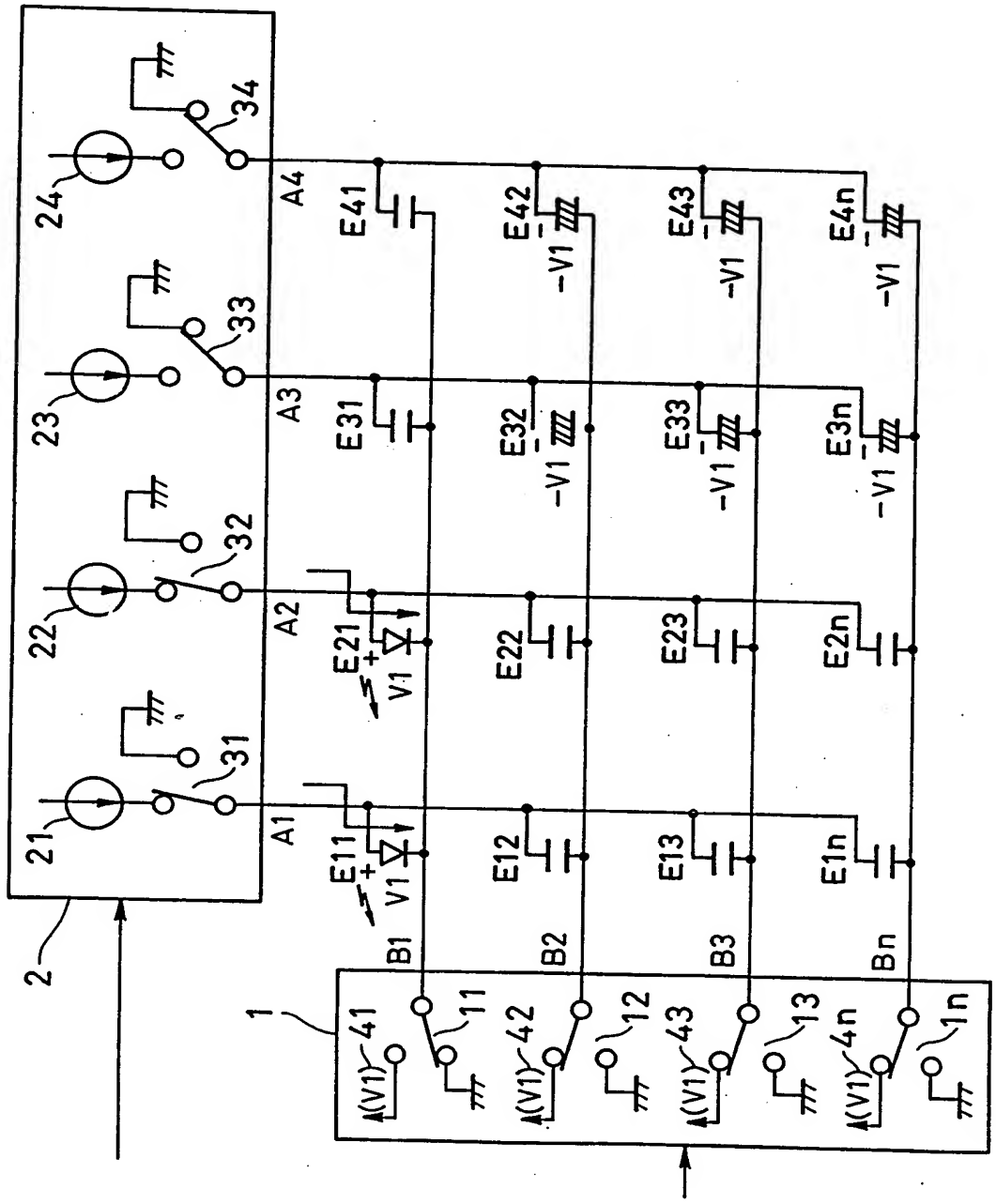


FIG. 4

PRIOR ART

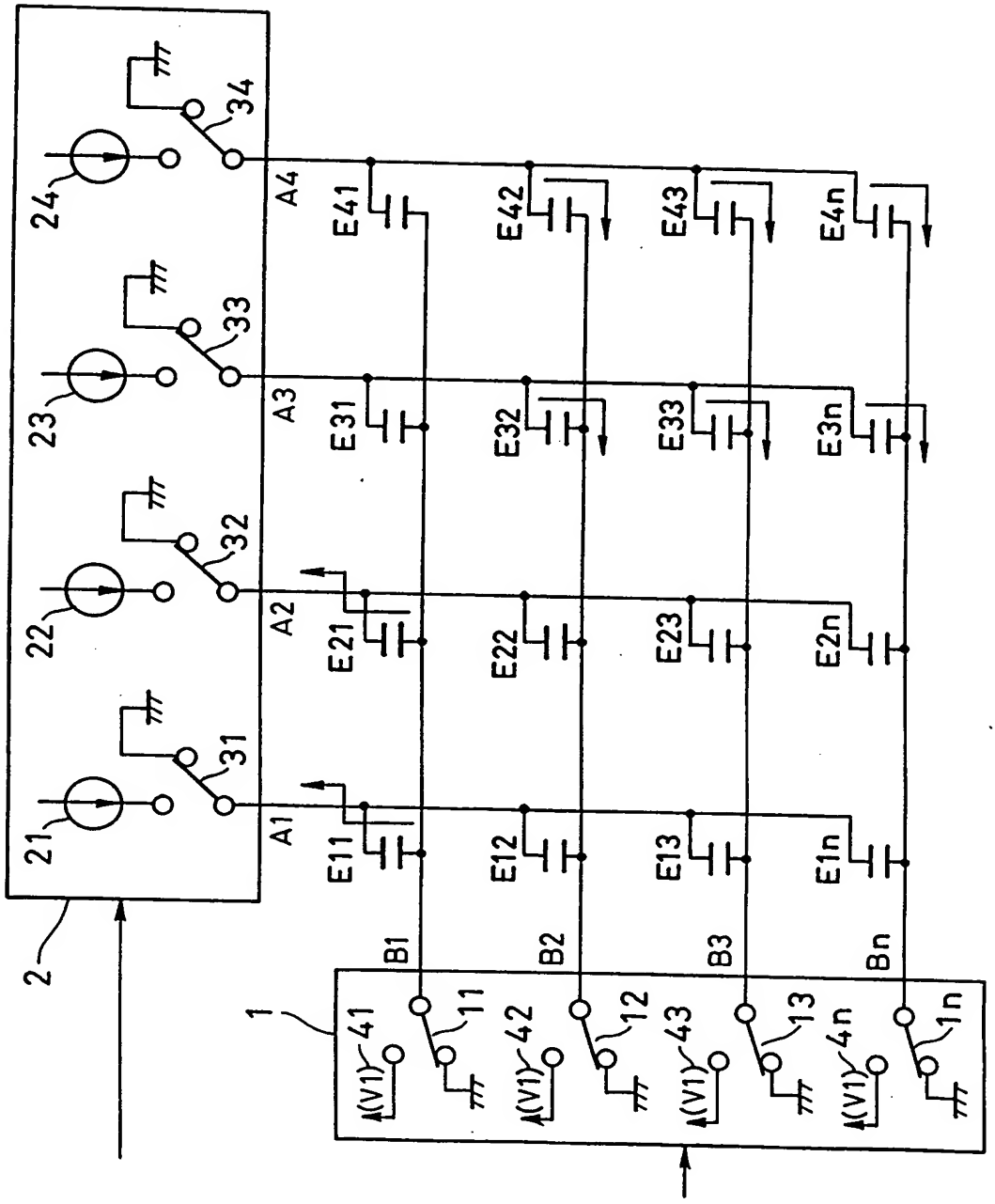


FIG.5 PRIOR ART

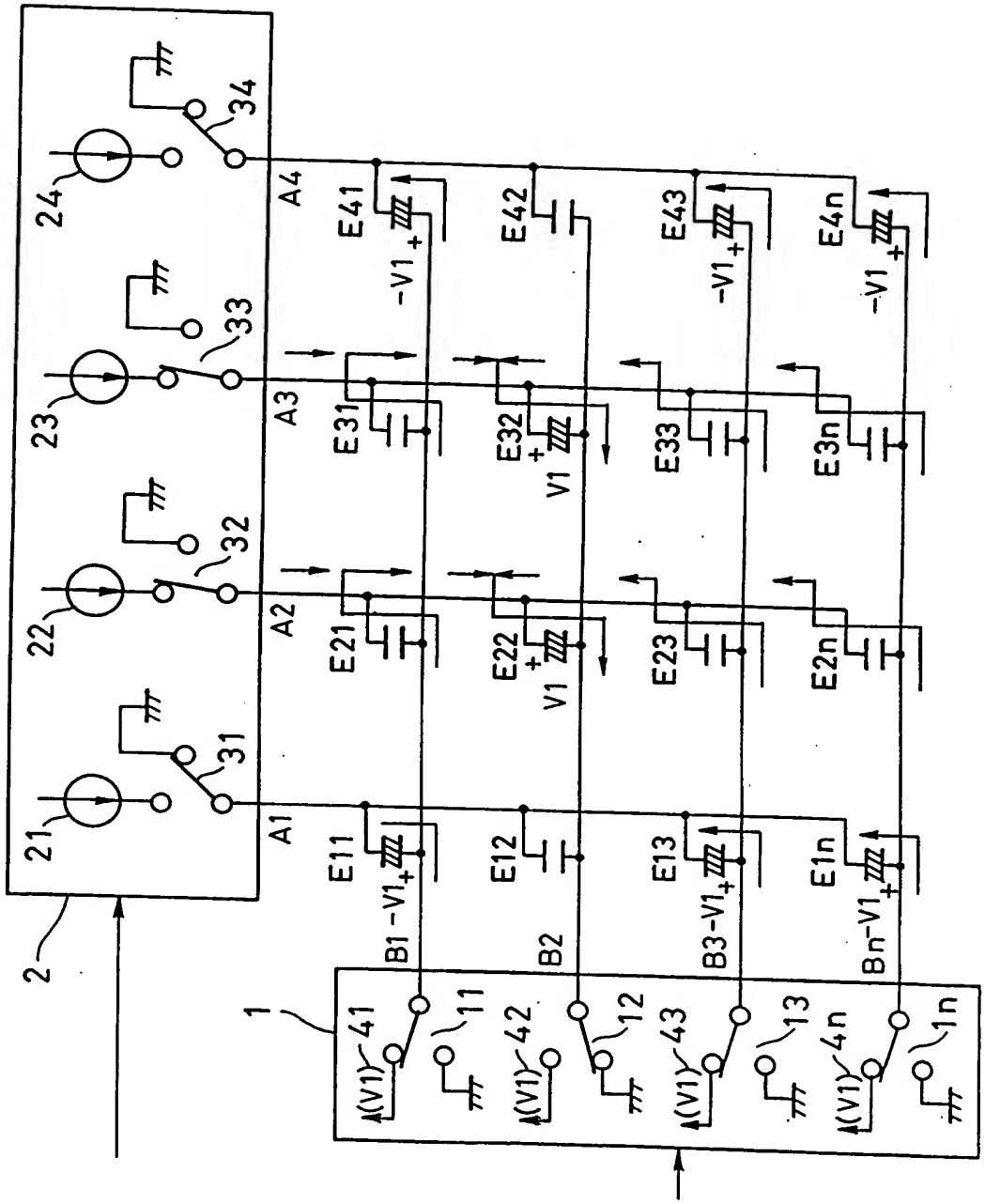


FIG. 6

PRIOR ART

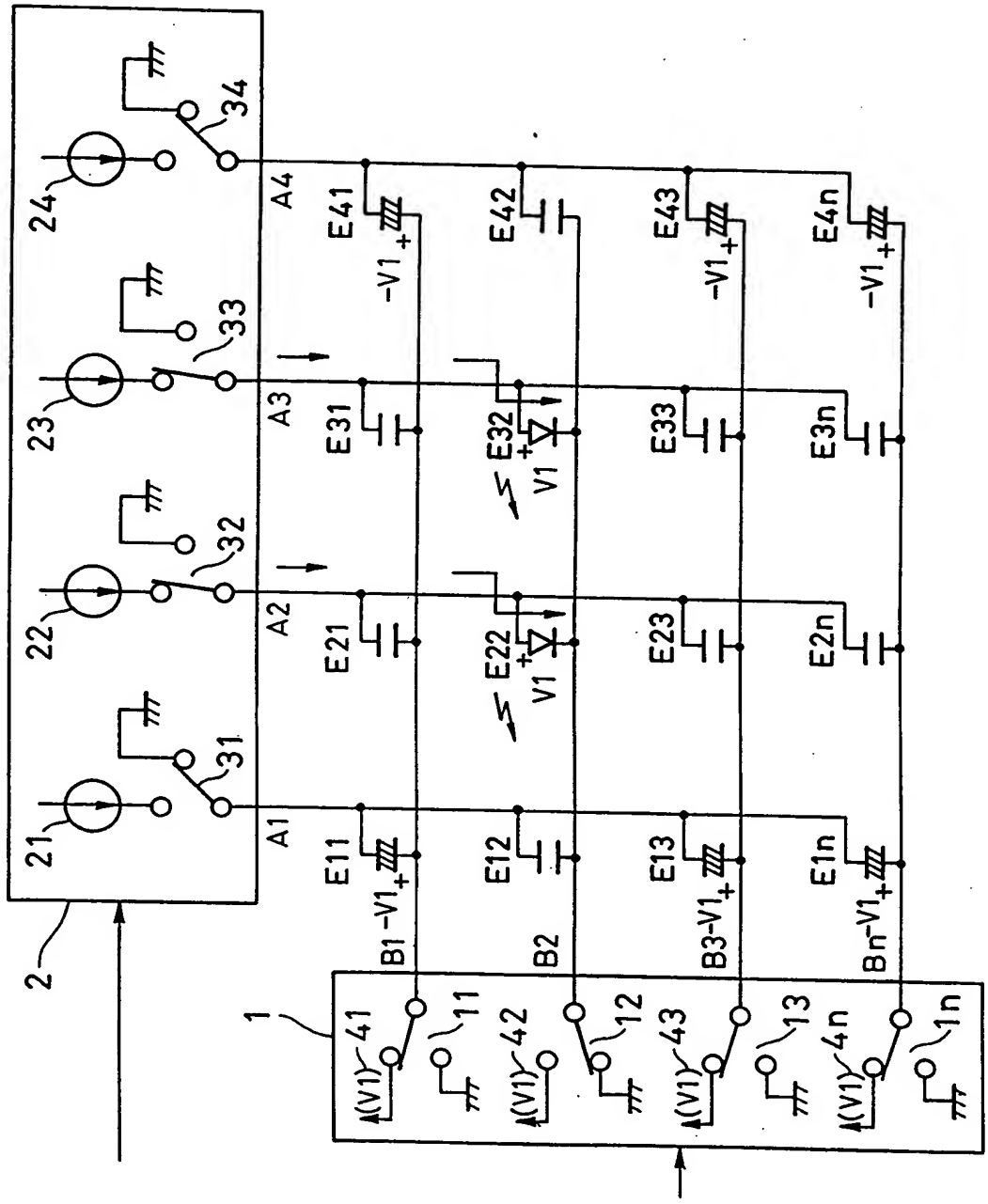


FIG.7A

PRIOR ART

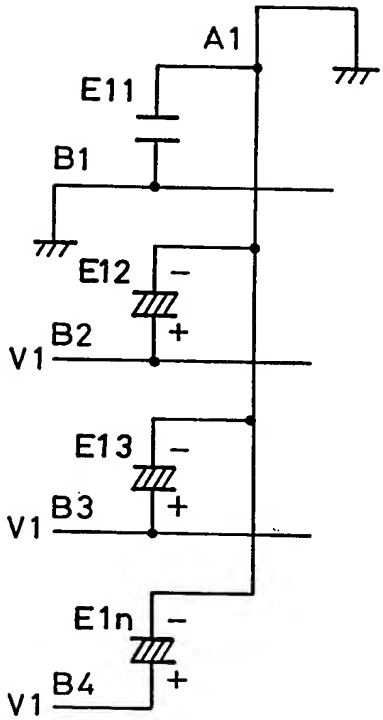


FIG.7B

PRIOR ART

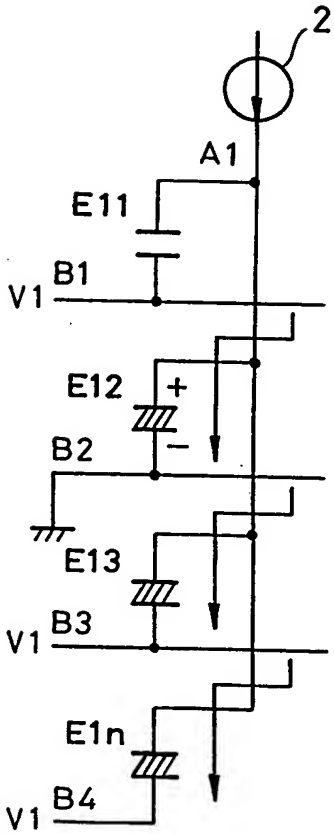


FIG. 8

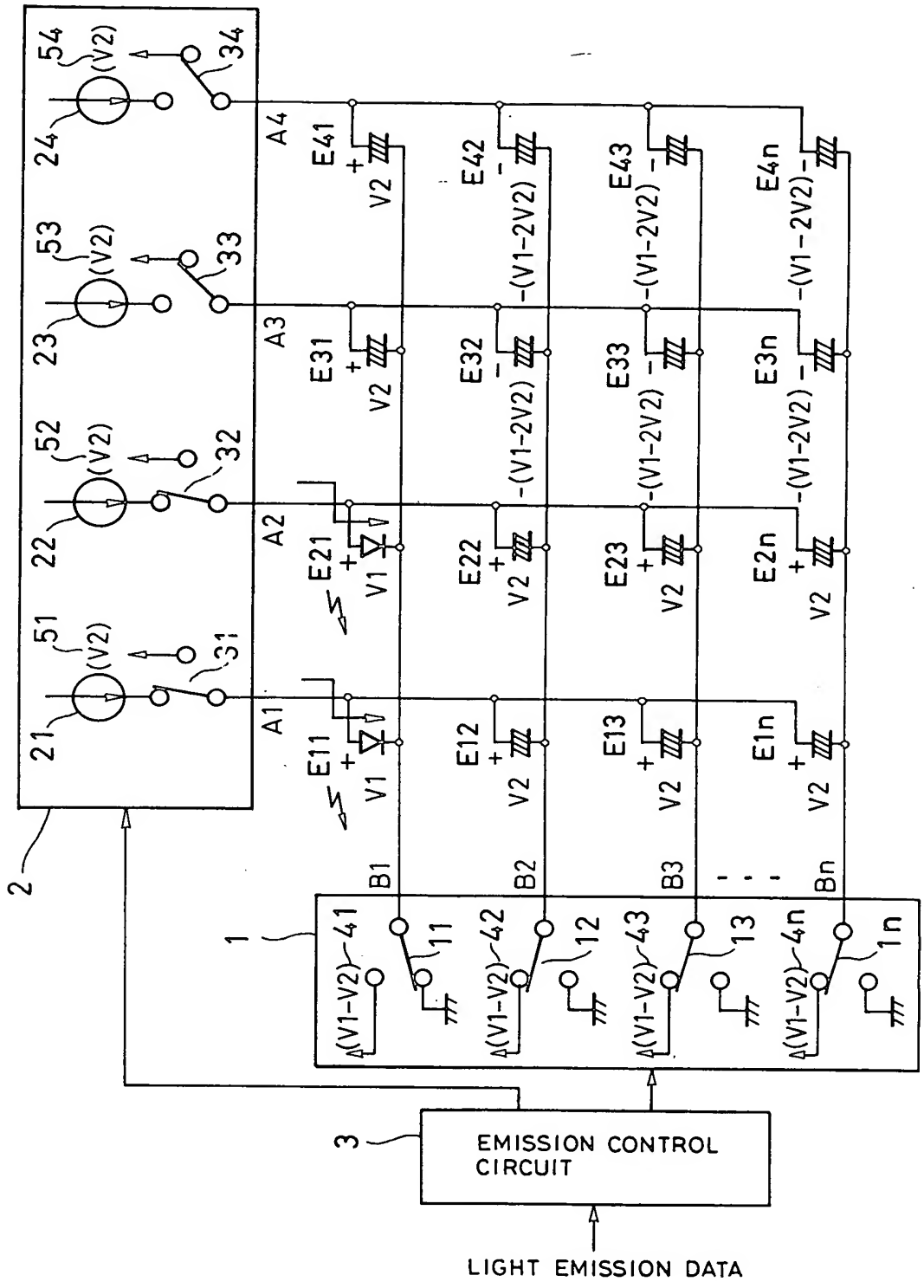

2
1
3
EMISSION CONTROL CIRCUIT
LIGHT EMISSION DATA
A1
A2
A3
A4
B1
B2
B3
Bn
E11
E12
E13
E1n
E21
E22
E23
E2n
E31
E32
E33
E3n
E41
E42
E43
E4n
V1
V2
-(V1-2V2)
(V1-V2)
11
12
13
1n
21
22
23
24
31
32
33
34
41
42
43
4n
51
52
53
54

FIG. 9

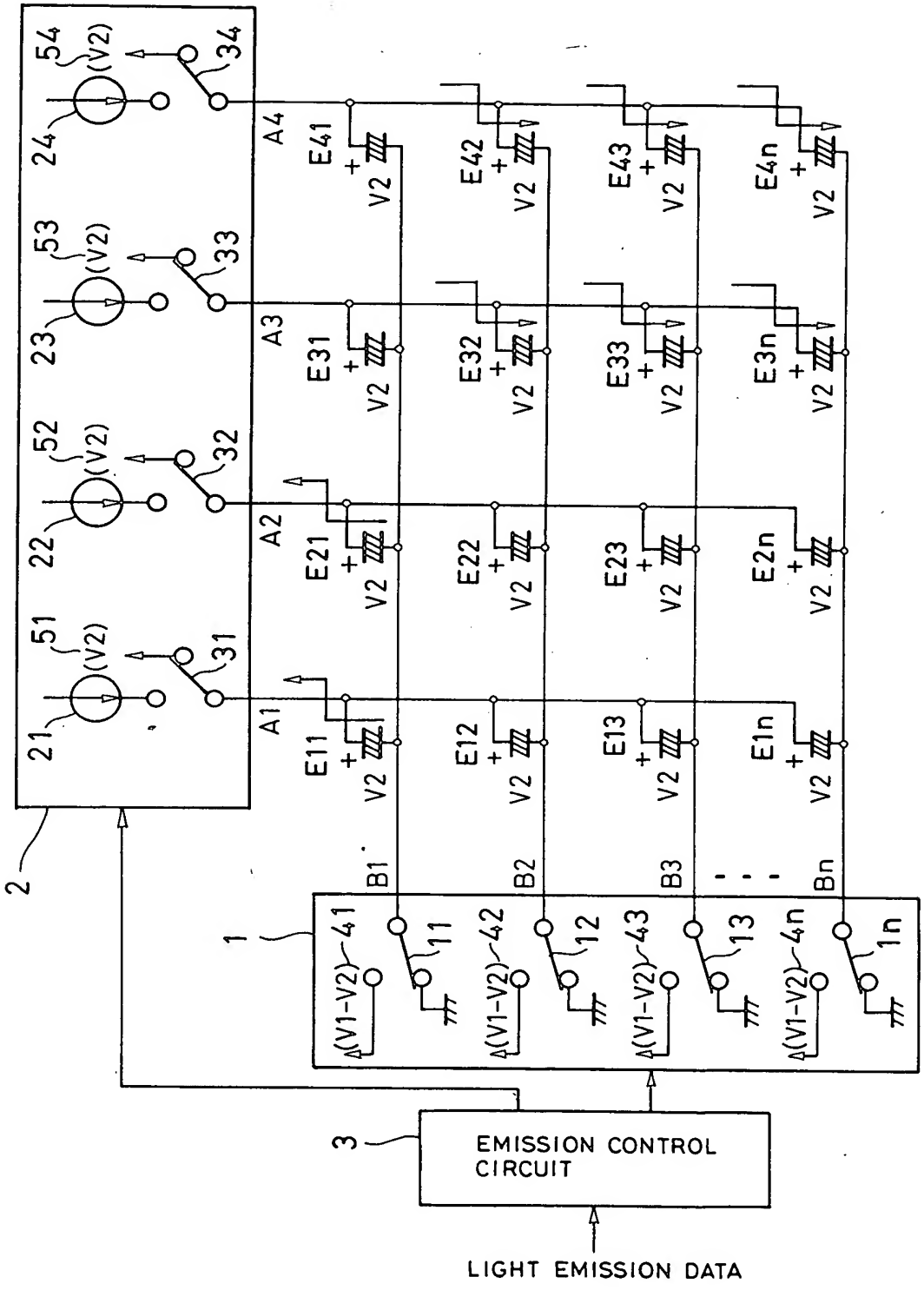

2
1
3
EMISSION CONTROL CIRCUIT
LIGHT EMISSION DATA
A1
A2
A3
A4
B1
B2
B3
Bn
V2
(V2)
(V1-V2)
E11
E12
E13
E1n
E21
E22
E23
E2n
E31
E32
E33
E3n
E41
E42
E43
E4n
11
12
13
1n
21
22
23
24
31
32
33
34
41
42
43
4n
51
52
53
54

FIG.10

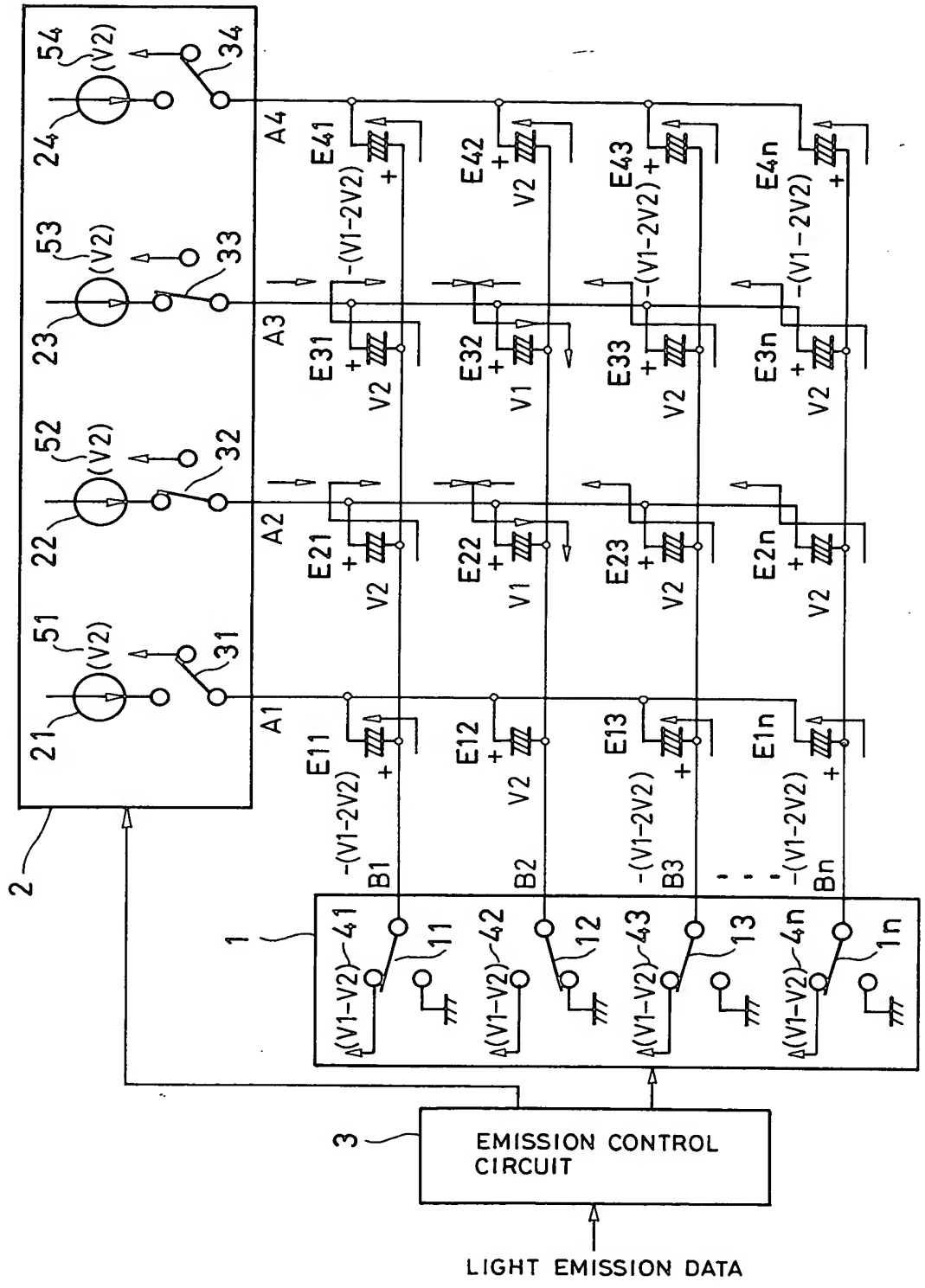


FIG. 11

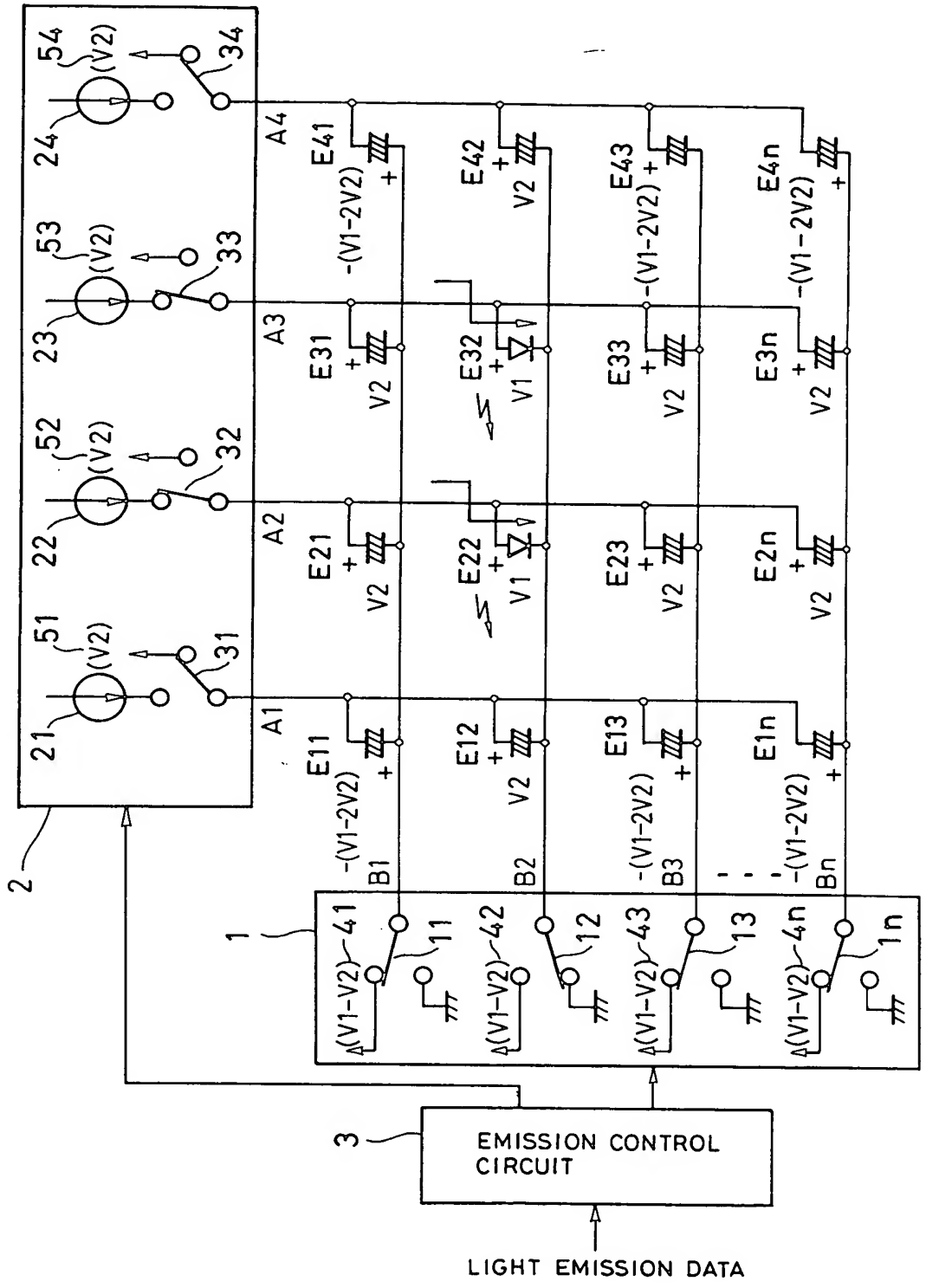

2
1
3
EMISSION CONTROL CIRCUIT
LIGHT EMISSION DATA
A1
A2
A3
A4
B1
B2
B3
Bn
E11
E12
E13
E1n
E21
E22
E23
E2n
E31
E32
E33
E3n
E41
E42
E43
E4n
V1
V2
-(V1-2V2)
Δ(V1-V2)
11
12
13
1n
21
22
23
24
31
32
33
34
41
42
43
4n
51
52
53
54

FIG.12

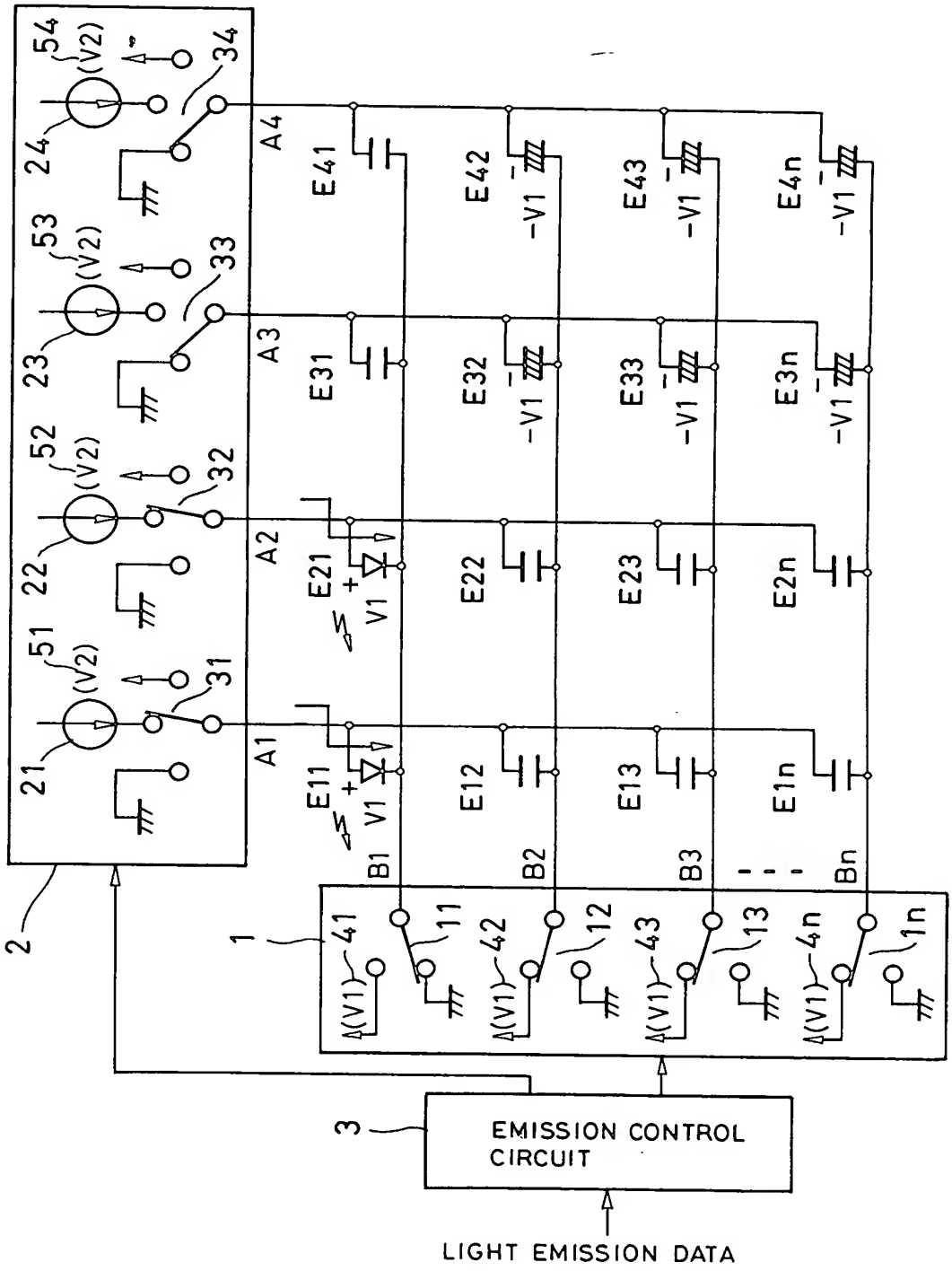


FIG. 13

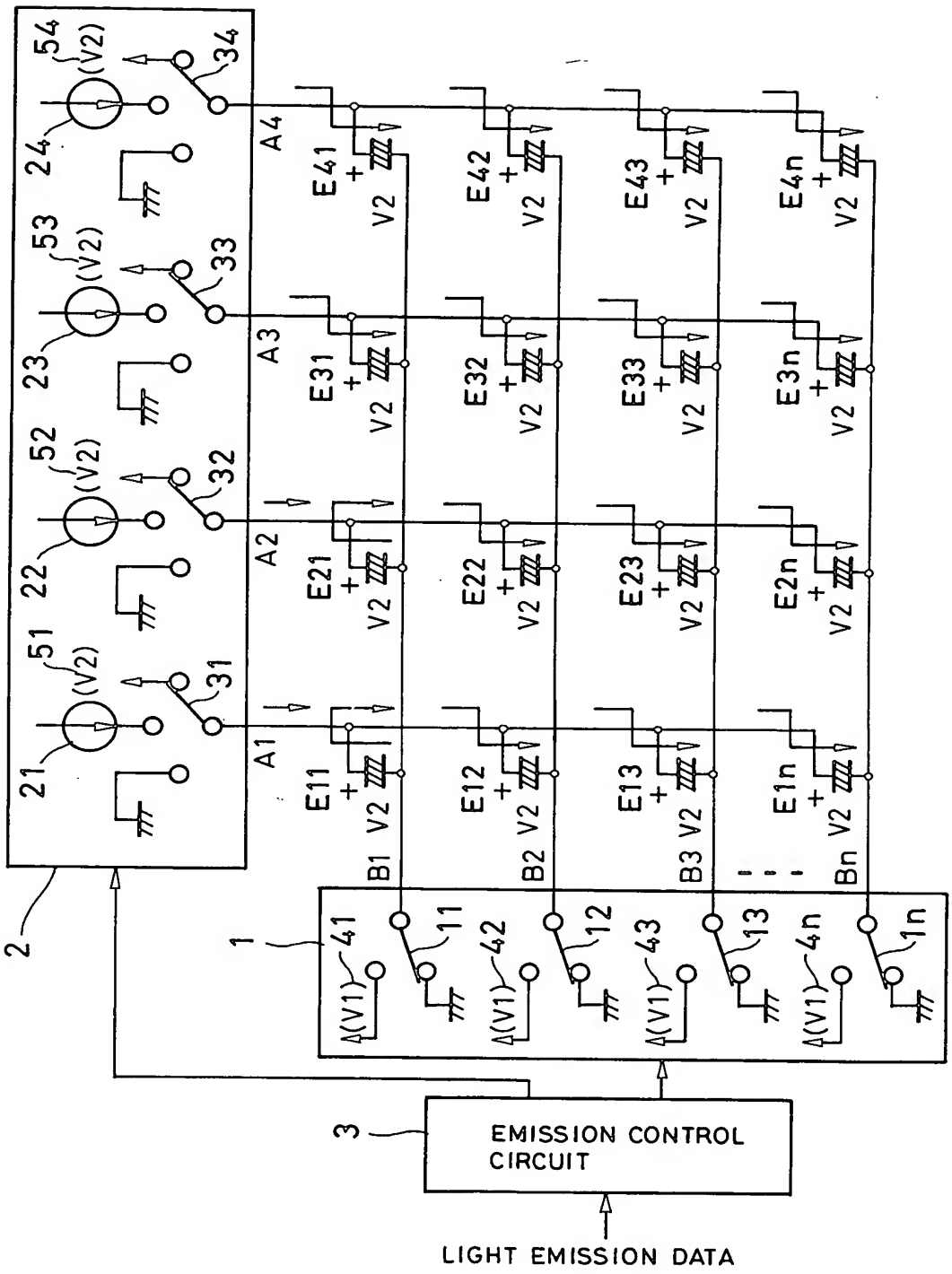

2
1
3
EMISSION CONTROL CIRCUIT
LIGHT EMISSION DATA
A1
A2
A3
A4
B1
B2
B3
Bn
V2
(V2)
(V1)

FIG. 14

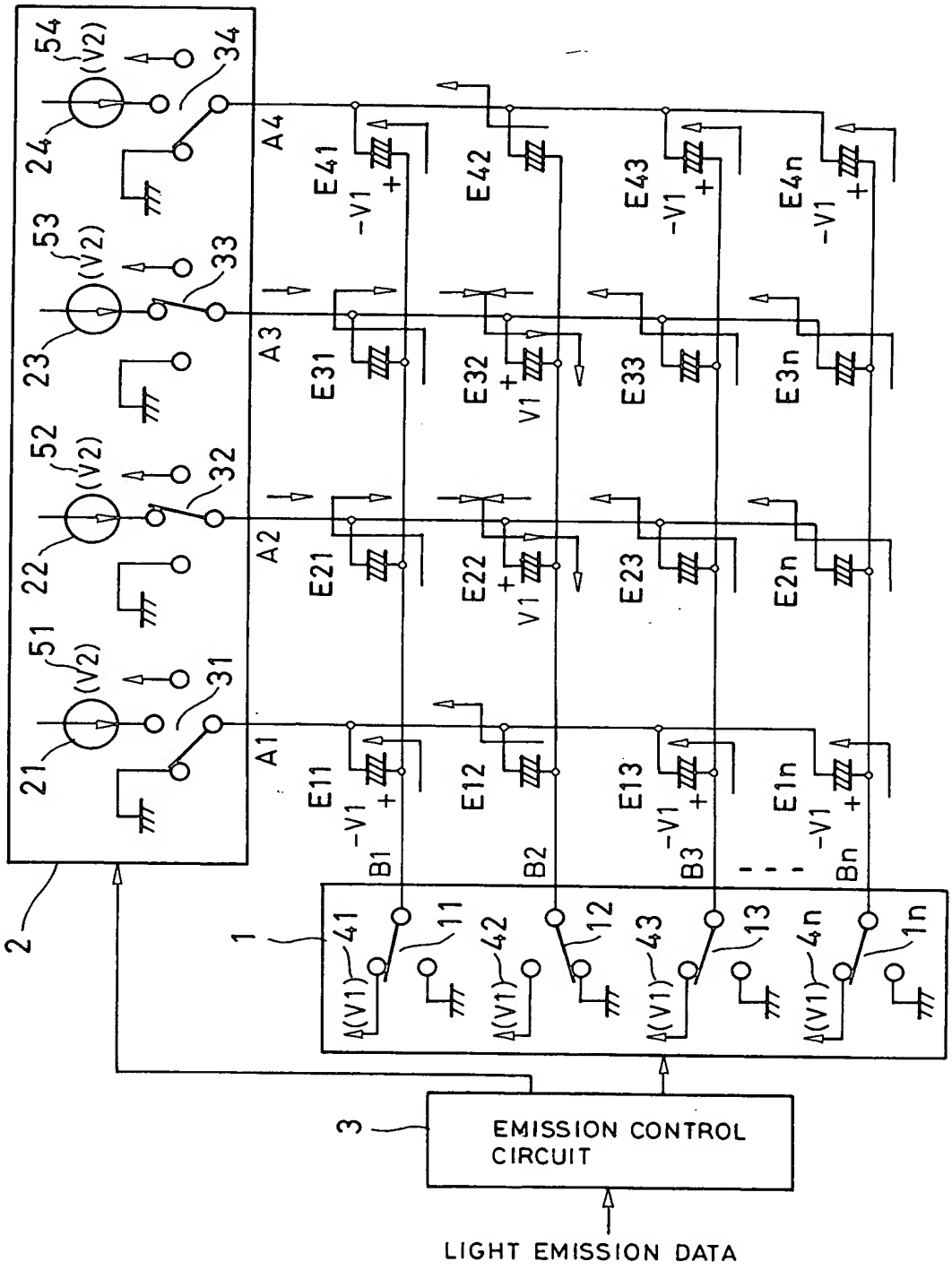

2
1
3
EMISSION CONTROL CIRCUIT
LIGHT EMISSION DATA
A1
A2
A3
A4
B1
B2
B3
Bn
E11
E12
E13
E1n
E21
E22
E23
E2n
E31
E32
E33
E3n
E41
E42
E43
E4n
-V1
+V1
(V1)
(V2)
11
12
13
1n
21
22
23
24
31
32
33
34
41
42
43
4n
51
52
53
54

FIG.15

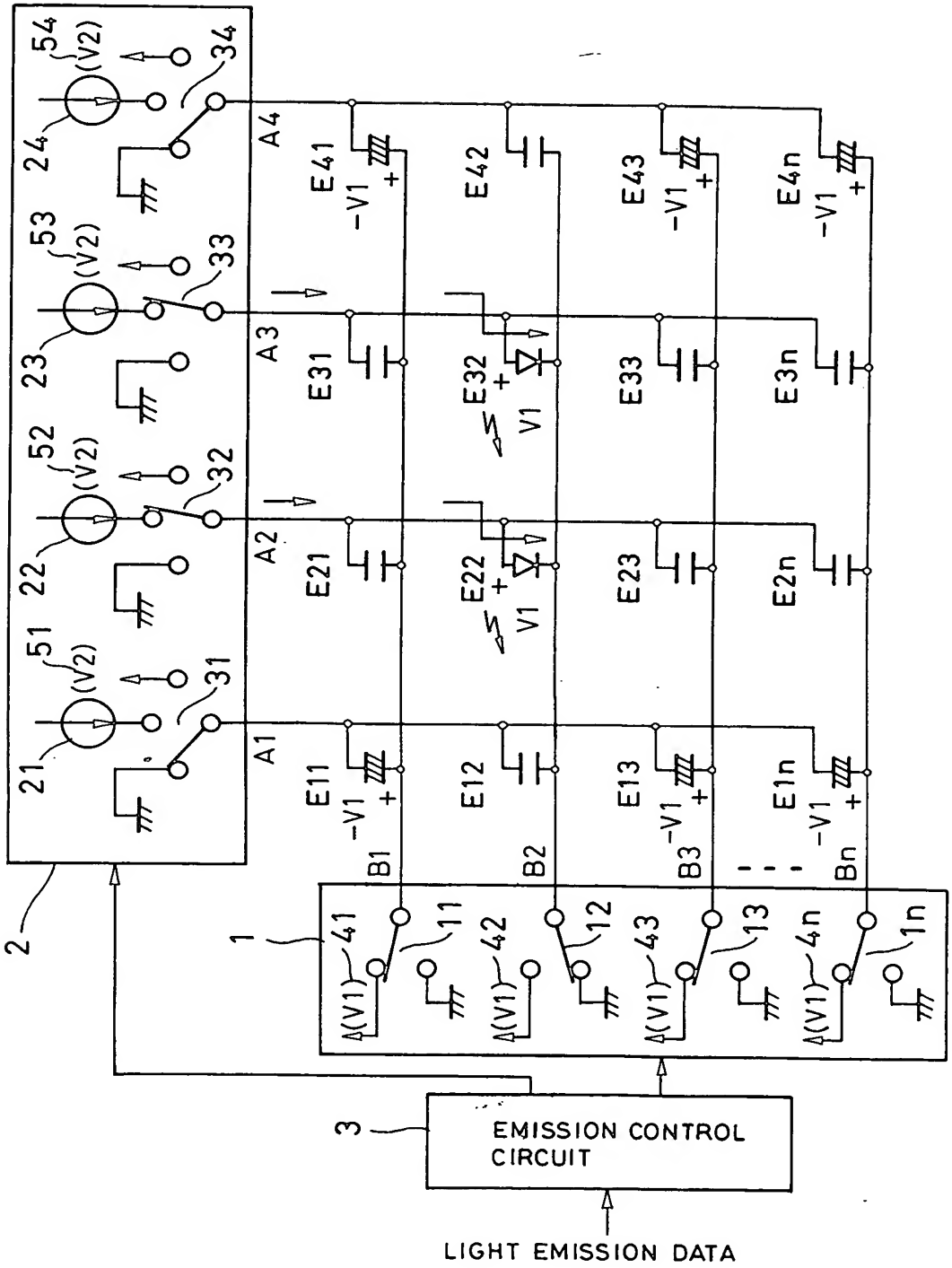


FIG.16

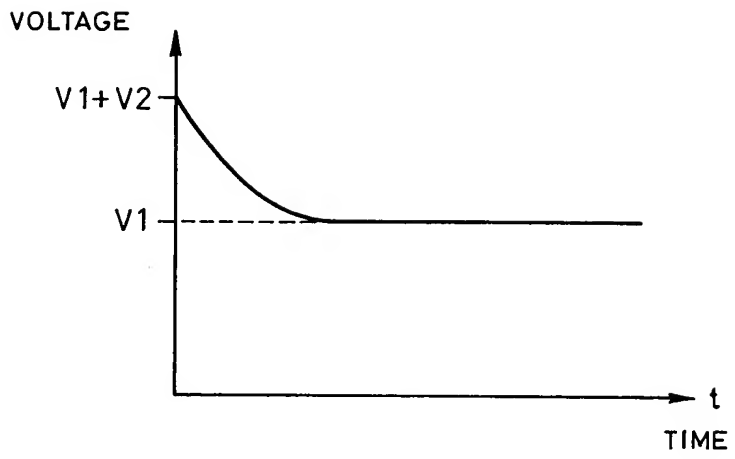
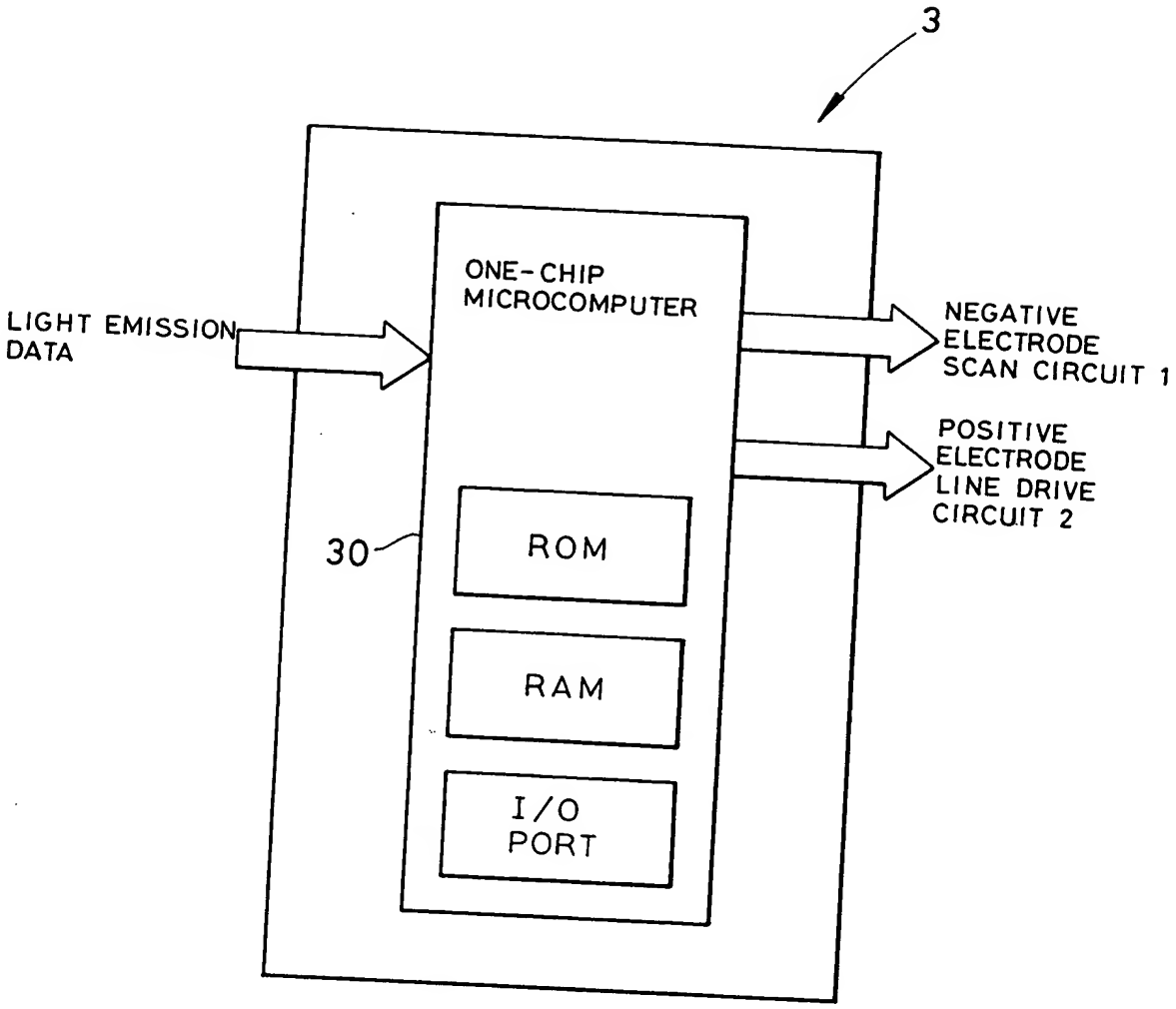


FIG.17


3
ONE-CHIP MICROCOMPUTER
LIGHT EMISSION DATA
NEGATIVE ELECTRODE SCAN CIRCUIT 1
POSITIVE ELECTRODE LINE DRIVE CIRCUIT 2
30
ROM
RAM
I/O PORT